Exhibit 9

Exhibit 11: U.S. Patent No. 8,666,062

Claim 1	Exemplary Evidence of Infringement
1[pre] A method of performing a finite field operation on elements of a finite field, the method comprising a processor:	Core Scientific, Inc. (hereinafter "Core") performs a method for finite field operation on elements of a finite field, for example, during the transfer of Bitcoin to an address, which is a cryptographic operation, using a processor. *See*, *e.g.*: "The Company sells bitcoin it receives through mining…. Sales of digital assets awarded to the Company through its self-mining activities are classified as cash flows from operating activities. The Company does not have any off-balance sheet holdings of digital assets and does not safeguard digital assets for third parties." *See*, *e.g.*, Core Scientific., Inc., Quarterly report pursuant to Section 13 and 15(d), (Form 10-Q), at Note 1, filed Nov. 06, 2024, available at https://www.sec.gov/ix?doc=/Archives/edgar/data/1839341/000162828024045811/core-20240930.htm "Core Scientific, Inc. is a leader in digital infrastructure for bitcoin mining and high-performance computing. We operate dedicated, purpose-built facilities for digital asset mining and are a premier provider of digital infrastructure, software solutions and services to our third-party customers. We employ are own large fleet of computers ('miners') to earn digital assets for our own account and we provide hosting services for large bitcoin mining customers …. We derive the majority of our revenue from earning bitcoin for our own account ('self-mining')." *See*, *e.g.*, Core Scientific., Inc., Quarterly report pursuant to Section 13 and 15(d), (Form 10-Q), at Note 1, filed Nov. 06, 2024, available at https://www.sec.gov/ix?doc=/Archives/edgar/data/1839341/000162828024045811/core-20240930.htm We finance our operations primarily through cash generated from operations, including the sale of self-mined bitcoin …." *See*, *e.g.*, Core Scientific, Inc. Form 10-K, at 67, filed Feb. 27, 2025, available at https://investors.corescientific.com/sec-filings/all-sec-filings/content/0001628280-25-008302/0001628280-25-008302.pdf

Claim 1	Exemplary Evidence of Infringement

	"**Bitcoin signed messages have three parts, which are the Message, Address, and Signature**. The message is the actual message text - all kinds of text is supported, but it is recommended to avoid using non-ASCII characters in the signature because they might be encoded in different character sets, preventing signature verification from succeeding.
	
	The address is a legacy, nested segwit, or native segwit address. Message signing from legacy addresses was added by Satoshi himself and therefore does not have a BIP. **Message signing from segwit addresses has been added by BIP137** … **The Signature is a base64-encoded ECDSA signature** that, when decoded, with fields described in the next section." (Emphasis added)
	
	See, *e.g.,* Message Signing, https://en.bitcoin.it/wiki/Message_signing.
	
	"This document describes a signature format for **signing messages with Bitcoin private keys**.
	
	The specification is intended to describe the standard for signatures of messages that can be signed and verified between different clients that exist in the field today." (Emphasis added)
	
	See, *e.g.*, Bitcoin BIP137, https://github.com/bitcoin/bips/blob/master/bip-0137.mediawiki.
	
	In secp256k1, type secp256k1_fe consists of 5 or 10 machine words, depending on the machine's word size: "field_5x52.h" applies to machines with 64bit word size and "field_10x32.h" applies to machines with 32bit word size. *See*, *e.g.*:
	
	`/** This field implementation represents the value as 5 uint64_t limbs in base`
	`* 2^52. */`
	`typedef struct {`
	`/* A field element f represents the sum(i=0..4, f.n[i] << (i*52)) mod p,`
	`* where p is the field modulus, 2^256 - 2^32 - 977.`
	`*`
	`* The individual limbs f.n[i] can exceed 2^52; the field's magnitude roughly`
	`* corresponds to how much excess is allowed. The value`
	`* sum(i=0..4, f.n[i] << (i*52)) may exceed p, unless the field element is`
	`* normalized. */`
	`uint64_t n[5];`

Claim 1	Exemplary Evidence of Infringement

```
    /*
     * Magnitude m requires:
     * n[i] <= 2 * m * (2^52 - 1) for i=0..3
     * n[4] <= 2 * m * (2^48 - 1)
     *
     * Normalized requires:
     * n[i] <= (2^52 - 1) for i=0..3
     * sum(i=0..4, n[i] << (i*52)) < p
     * (together these imply n[4] <= 2^48 - 1)
     */
    SECP256K1_FE_VERIFY_FIELDS
} secp256k1_fe;
```

See, *e.g.*, bitcoin/src/secp256k1/src/field_5x52.h

"The points on the elliptic curve are the pairs of finite field elements."
See, *e.g.*, '062 pat. at col. 1, lines 50-52.

```
/** A group element in affine coordinates on the secp256k1 curve,
 * or occasionally on an isomorphic curve of the form y^2 = x^3 + 7*t^6.
 * ...
 */
typedef struct {
    secp256k1_fe x;
    secp256k1_fe y;
    int infinity; /* whether this represents the point at infinity */
} secp256k1_ge;

...

/** A group element of the secp256k1 curve, in jacobian coordinates.
 * ...
 */
typedef struct {
    secp256k1_fe x; /* actual X: x/z^2 */
    secp256k1_fe y; /* actual Y: y/z^3 */
    secp256k1_fe z;
    int infinity; /* whether this represents the point at infinity */
} secp256k1_gej;
```

Claim 1	Exemplary Evidence of Infringement
	See, *e.g.*, bitcoin/src/secp256k1/src/group.h Core performs the method using a processor. *See*, *e.g.*: "The miners we operate are highly specialized computer servers built to use application-specific integrated circuit ("ASIC") chips that are designed specifically to mine bitcoin. With miners we produce computing power, known as "hash rate," with which we verify transactions on the Bitcoin blockchain. Bitcoin "mining" refers to the process of proposing and verifying transaction updates to the Bitcoin blockchain, which helps keep the Bitcoin network and its blockchain secure. Our bitcoin mining operation is focused on the generation of bitcoin by solving complex cryptographic algorithms to validate transactions on the Bitcoin network blockchain, which is commonly referred to as "mining." *See*, *e.g.*, Core Scientific, Inc. Form 10-K, at 6, filed Feb. 27, 2025, available at https://investors.corescientific.com/sec-filings/all-sec-filings/content/0001628280-25-008302/0001628280-25-008302.pdf

1[a] obtaining a first set of instructions for performing the finite field operation on values representing the elements of the finite field;	Core obtains a first set of instructions for performing the finite field operation on values representing the elements of the finite field.
	
	For example, Core's miners obtain a first set of instructions (e.g., for executing `secp256k1_ge_set_gej`) for performing the finite field operation on values representing the elements of the finite field.
	
	For example, in secp256k1, type secp256k1_fe consists of 5 or 10 machine words, depending on the machine's word size: "field_5x52.h" applies to machines with 64bit word size and "field_10x32.h" applies to machines with 32bit word size. *See*, *e.g.*:
	
	`/** This field implementation represents the value as 5 uint64_t limbs in base`
	`* 2^52. */`
	`typedef struct {`
	`/* A field element f represents the sum(i=0..4, f.n[i] << (i*52)) mod p,`

Claim 1	Exemplary Evidence of Infringement

```
     * where p is the field modulus, 2^256 - 2^32 - 977.
     *
     * The individual limbs f.n[i] can exceed 2^52; the field's magnitude roughly
     * corresponds to how much excess is allowed. The value
     * sum(i=0..4, f.n[i] << (i*52)) may exceed p, unless the field element is
     * normalized. */
    uint64_t n[5];
    /*
     * Magnitude m requires:
     * n[i] <= 2 * m * (2^52 - 1) for i=0..3
     * n[4] <= 2 * m * (2^48 - 1)
     *
     * Normalized requires:
     * n[i] <= (2^52 - 1) for i=0..3
     * sum(i=0..4, n[i] << (i*52)) < p
     * (together these imply n[4] <= 2^48 - 1)
     */
    SECP256K1_FE_VERIFY_FIELDS
} secp256k1_fe;
```

See, *e.g.*, bitcoin/src/secp256k1/src/field_5x52.h

"The points on the elliptic curve are the pairs of finite field elements."
See, *e.g.*, '062 pat. at col. 1, lines 50-52.

```
/** A group element in affine coordinates on the secp256k1 curve,
 * or occasionally on an isomorphic curve of the form y^2 = x^3 + 7*t^6.
 * ...
 */
typedef struct {
    secp256k1_fe x;
    secp256k1_fe y;
    int infinity; /* whether this represents the point at infinity */
} secp256k1_ge;

...

/** A group element of the secp256k1 curve, in jacobian coordinates.
 * ...
```

Claim 1	Exemplary Evidence of Infringement

```
 */
typedef struct {
    secp256k1_fe x; /* actual X: x/z^2 */
    secp256k1_fe y; /* actual Y: y/z^3 */
    secp256k1_fe z;
    int infinity; /* whether this represents the point at infinity */
} secp256k1_gej;
```

See, *e.g.*, bitcoin/src/secp256k1/src/group.h

The result of `secp256k1_ge_set_gej` is unreduced and needs normalizing. *See*, *e.g.*:

```
static int secp256k1_ecdsa_sig_sign(const secp256k1_ecmult_gen_context *ctx,
  secp256k1_scalar *sigr, secp256k1_scalar *sigs, const secp256k1_scalar *seckey,
  const secp256k1_scalar *message, const secp256k1_scalar *nonce, int *recid
) {
    ...; secp256k1_ge r; ...;
    secp256k1_ecmult_gen(ctx, &rp, nonce);
    secp256k1_ge_set_gej(&r, &rp);
    secp256k1_fe_normalize(&r.x);
    secp256k1_fe_normalize(&r.y);
    secp256k1_fe_get_b32(b, &r.x);
    secp256k1_scalar_set_b32(sigr, b, &overflow);
    ...; if (...) { *recid = (...) | secp256k1_fe_is_odd(&r.y); } ...;
}
```

See, *e.g.*, bitcoin/src/secp256k1/src/ecdsa_impl.h

The function `secp256k1_ge_set_gej` invokes `secp256k1_fe_mul`. That function invokes `secp256k1_fe_impl_mul`. That function invokes `secp256k1_fe_mul_inner`. Function `secp256k1_u128_accum_mul` is then invoked for each word of above finite field element **r** (typed `secp256k1_ge`). *See*, *e.g.*:

```
/* Multiply two unsigned 64-bit values a and b and write the result to r. */
static SECP256K1_INLINE void secp256k1_u128_mul(secp256k1_uint128 *r,
  uint64_t a, uint64_t b);
```

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	See, *e.g.*, bitcoin/src/secp256k1/src/int128.h

1[b] executing the first set of instructions to generate an unreduced result completing the finite field operation;	Core executes the first set of instructions to generate an unreduced result completing the finite field operation.
	
	For example, Core' miners execute the first set of instructions (e.g., for executing `secp256k1_ge_set_gej`) to generate an unreduced result completing the finite field operation.
	
	For example, the result of `secp256k1_ge_set_gej` is unreduced and needs normalizing. *See*, *e.g.*:
	
	`static int secp256k1_ecdsa_sig_sign(const secp256k1_ecmult_gen_context *ctx,`
	`  secp256k1_scalar *sigr, secp256k1_scalar *sigs, const secp256k1_scalar *seckey,`
	`  const secp256k1_scalar *message, const secp256k1_scalar *nonce, int *recid`
	`) {`
	`  ...; secp256k1_ge r; ...;`
	`  secp256k1_ecmult_gen(ctx, &rp, nonce);`
	`  secp256k1_ge_set_gej(&r, &rp);`
	`  secp256k1_fe_normalize(&r.x);`
	`  secp256k1_fe_normalize(&r.y);`
	`  secp256k1_fe_get_b32(b, &r.x);`
	`  secp256k1_scalar_set_b32(sigr, b, &overflow);`
	`  ...; if (...) { *recid = (...) | secp256k1_fe_is_odd(&r.y); } ...;`
	`}`
	
	See, *e.g.*, bitcoin/src/secp256k1/src/ecdsa_impl.h
	
	The function `secp256k1_ge_set_gej` invokes `secp256k1_fe_mul`. That function invokes `secp256k1_fe_impl_mul`. That function invokes `secp256k1_fe_mul_inner`. Function `secp256k1_u128_accum_mul` is then invoked for each word of above finite field element **r** (typed `secp256k1_ge`). *See*, *e.g.*:
	
	`/* Multiply two unsigned 64-bit values a and b and write the result to r. */`
	`static SECP256K1_INLINE void secp256k1_u128_mul(secp256k1_uint128 *r,`
	`  uint64_t a, uint64_t b);`
	
	See, *e.g.*, bitcoin/src/secp256k1/src/int128.h

Claim 1	Exemplary Evidence of Infringement

1[c] obtaining a second set of instructions for performing a modular reduction for a specific finite field;	Core obtains a second set of instructions for performing a modular reduction for a specific finite field.
	
	For example, Core's miners obtain a second set of instructions (*e.g.*, for executing `secp256k1_fe_normalize`) for performing a modular reduction for a specific finite field.
	
	For example, the result of `secp256k1_ge_set_gej` is unreduced and needs normalizing. *See*, *e.g.*:
	
	`static int secp256k1_ecdsa_sig_sign(const secp256k1_ecmult_gen_context *ctx,`
	`  secp256k1_scalar *sigr, secp256k1_scalar *sigs, const secp256k1_scalar *seckey,`
	`  const secp256k1_scalar *message, const secp256k1_scalar *nonce, int *recid`
	`) {`
	`  ...; secp256k1_ge r; ...;`
	`  secp256k1_ecmult_gen(ctx, &rp, nonce);`
	`  secp256k1_ge_set_gej(&r, &rp);`
	`  secp256k1_fe_normalize(&r.x);`
	`  secp256k1_fe_normalize(&r.y);`
	`  secp256k1_fe_get_b32(b, &r.x);`
	`  secp256k1_scalar_set_b32(sigr, b, &overflow);`
	`  ...; if (...) { *recid = (...) | secp256k1_fe_is_odd(&r.y); } ...;`
	`}`
	
	See, *e.g.*, bitcoin/src/secp256k1/src/ecdsa_impl.h
	
	The function `secp256k1_ge_set_gej` invokes `secp256k1_fe_mul`. That function invokes `secp256k1_fe_impl_mul`. That function invokes `secp256k1_fe_mul_inner`. Function `secp256k1_u128_accum_mul` is then invoked for each word of above finite field element **r** (typed `secp256k1_ge`). *See*, *e.g.*:
	
	`/* Multiply two unsigned 64-bit values a and b and write the result to r. */`
	`static SECP256K1_INLINE void secp256k1_u128_mul(secp256k1_uint128 *r,`
	`  uint64_t a, uint64_t b);`
	
	See, *e.g.*, bitcoin/src/secp256k1/src/int128.h

Claim 1	Exemplary Evidence of Infringement

	The function `secp256k1_fe_impl_normalize` ensures a field element does not exceed "`field modulus, 2^256 - 2^32 - 977`", per "`field_5x52.h`". *See*, *e.g.*:
	
	`SECP256K1_INLINE static void secp256k1_fe_normalize(secp256k1_fe *r) {`
	`  ...; secp256k1_fe_impl_normalize(r); ...;`
	`}`
	
	See, *e.g.*, bitcoin/src/secp256k1/src/field_impl.h
	
	`static void secp256k1_fe_impl_normalize(secp256k1_fe *r) {`
	`  uint64_t t0 = r->n[0], t1 = r->n[1], t2 = r->n[2], t3 = r->n[3], t4 = r->n[4];`
	`  /* Reduce t4 at the start so there will be at most a single carry from the first pass */ ...;`
	`  /* The first pass ensures the magnitude is 1, ... */ ...;`
	`  /* ... except for a possible carry at bit 48 of t4 (i.e. bit 256 of the field element) */ ...;`
	`  /* At most a single final reduction is needed; check if the value is >= the field characteristic */ ...;`
	`  /* Apply the final reduction (for constant-time behaviour, we do it always) */ ...;`
	`  /* If t4 didn't carry to bit 48 already, then it should have after any final reduction */ ...;`
	`  /* Mask off the possible multiple of 2^256 from the final reduction */ ...;`
	`  r->n[0] = t0; r->n[1] = t1; r->n[2] = t2; r->n[3] = t3; r->n[4] = t4;`
	`}`
	
	See, *e.g.*, bitcoin/src/secp256k1/src/field_5x52_impl.h

1[d] executing the second set of instructions on the unreduced result to generate a reduced result; and	Core executes the second set of instructions on the unreduced result to generate a reduced result.
	
	For example, Core's miners execute the second set of instructions (*e.g.*, for executing `secp256k1_fe_normalize`) on the unreduced result to generate a reduced result.
	
	For example, the result of `secp256k1_ge_set_gej` is unreduced and needs normalizing. *See*, *e.g.*:
	
	`static int secp256k1_ecdsa_sig_sign(const secp256k1_ecmult_gen_context *ctx,`
	`  secp256k1_scalar *sigr, secp256k1_scalar *sigs, const secp256k1_scalar *seckey,`
	`  const secp256k1_scalar *message, const secp256k1_scalar *nonce, int *recid`
	`) {`

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```
    ...; secp256k1_ge r; ...;
    secp256k1_ecmult_gen(ctx, &rp, nonce);
    secp256k1_ge_set_gej(&r, &rp);
    secp256k1_fe_normalize(&r.x);
    secp256k1_fe_normalize(&r.y);
    secp256k1_fe_get_b32(b, &r.x);
    secp256k1_scalar_set_b32(sigr, b, &overflow);
    ...; if (...) { *recid = (...) | secp256k1_fe_is_odd(&r.y); } ...;
}
```

See, *e.g.*, bitcoin/src/secp256k1/src/ecdsa_impl.h

The function `secp256k1_ge_set_gej` invokes `secp256k1_fe_mul`. That function invokes `secp256k1_fe_impl_mul`. That function invokes `secp256k1_fe_mul_inner`. Function `secp256k1_u128_accum_mul` is then invoked for each word of above finite field element **r** (typed `secp256k1_ge`). *See*, *e.g.*:

```
/* Multiply two unsigned 64-bit values a and b and write the result to r. */
static SECP256K1_INLINE void secp256k1_u128_mul(secp256k1_uint128 *r,
  uint64_t a, uint64_t b);
```

See, *e.g.*, bitcoin/src/secp256k1/src/int128.h

The function `secp256k1_fe_impl_normalize` ensures a field element does not exceed "`field modulus, 2^256 - 2^32 - 977`", per "`field_5x52.h`". *See*, *e.g.*:

```
SECP256K1_INLINE static void secp256k1_fe_normalize(secp256k1_fe *r) {
    ...; secp256k1_fe_impl_normalize(r); ...;
}
```

See, *e.g.*, bitcoin/src/secp256k1/src/field_impl.h

```
static void secp256k1_fe_impl_normalize(secp256k1_fe *r) {
    uint64_t t0 = r->n[0], t1 = r->n[1], t2 = r->n[2], t3 = r->n[3], t4 = r->n[4];
    /* Reduce t4 at the start so there will be at most a single carry from the first
       pass */ ...;
    /* The first pass ensures the magnitude is 1, ... */ ...;
```

Claim 1	Exemplary Evidence of Infringement
	/* ... except for a possible carry at bit 48 of t4 (i.e. bit 256 of the field element) */ ...; /* At most a single final **reduction is needed**; check if the value is >= the field characteristic */ ...; /* Apply the final reduction (for constant-time behaviour, we do it always) */ ...; /* If t4 didn't carry to bit 48 already, then it should have after any final reduction */ ...; /* Mask off the possible multiple of 2^256 from the final reduction */ ...; **r->n[0]** = t0; **r->n[1]** = t1; **r->n[2]** = t2; **r->n[3]** = t3; **r->n[4]** = t4; } *See*, *e.g.*, bitcoin/src/secp256k1/src/field_5x52_impl.h

1[e] providing the reduced result as an output for use in a cryptographic operation.	Core provides the reduced result as an output for use in a cryptographic operation.
	
	For example, Core's miners provide the reduced result as an output for use in a cryptographic operation.
	
	For example, the result of `secp256k1_ge_set_gej` is unreduced and needs normalizing. *See*, *e.g.*:
	
	static int secp256k1_ecdsa_sig_sign(const secp256k1_ecmult_gen_context *ctx,
	secp256k1_scalar *sigr, secp256k1_scalar *sigs, const secp256k1_scalar *seckey,
	const secp256k1_scalar *message, const secp256k1_scalar *nonce, int *recid
	) {
	...; secp256k1_ge r; ...;
	secp256k1_ecmult_gen(ctx, &rp, nonce);
	secp256k1_ge_set_gej(&r, &rp);
	secp256k1_fe_normalize(&r.x);
	secp256k1_fe_normalize(&r.y);
	secp256k1_fe_get_b32(b, &r.x);
	secp256k1_scalar_set_b32(sigr, b, &overflow);
	...; if (...) { ***recid** = (...) | secp256k1_fe_is_odd(&r.y); } ...;
	}
	
	See, *e.g.*, bitcoin/src/secp256k1/src/ecdsa_impl.h
	
	The function `secp256k1_ge_set_gej` invokes `secp256k1_fe_mul`. That function invokes `secp256k1_fe_impl_mul`. That function invokes `secp256k1_fe_mul_inner`. Function

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	`secp256k1_u128_accum_mul` is then invoked for each word of above finite field element **r** (typed `secp256k1_ge`). *See*, *e.g.*:
	
	`/* Multiply two unsigned 64-bit values a and b and write the result to r. */`
	`static SECP256K1_INLINE void secp256k1_u128_mul(secp256k1_uint128 *r,`
	`  uint64_t a, uint64_t b);`
	
	See, *e.g.*, bitcoin/src/secp256k1/src/int128.h